


Search Notes 	Application/Control No. 10560920	Applicant(s)/Patent Under Reexamination JEONG, SEONG IK.
	Examiner YUK CHOW	Art Unit 2629

SEARCHED			
Class	Subclass	Date	Examiner
345	55, 100	9/28/2010	YC
365	189, 230	9/28/2010	YC

SEARCH NOTES		
Search Notes	Date	Examiner
Display memory, addressing column in groups to reduce peak current.	9/28/2010	YC
Maintain rejection: cited ref, latch circuit read on claimed limitaion: transfer gates, since they have same function, regardless of the different structure.	4/23/2011	YC

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner

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